PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known					
Application Number	10/689,382				
Filing Date	20 October 2003				
First Named Inventor	Mark Beaumont				
Art Unit	Not yet assigned				
Examiner Name	Not yet assigned				
Attorney Docket Number	DB001064-000				

		2	U. S. PATEN	T DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/534/		US- 6,078,945	06/20/2000	Hinsley	
/EW/		US-	00/20/2000	Timoley	
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Examiner Cite		Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines. Where Relevant Passages		
_		Country Code ³ Number ⁴ 'Kind Code ³ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T
/EW/		WO 01/088696 A3	11/22/2001	Smith		
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Examiner Signature	/Eric Wai/	Date Considered	08/08/2007

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Sheet 1

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(==1.5.1)	ļ	Number-Kind Code ^{2 (f known)}			Figures Appear
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/EW/		^{US-} 5,630,129	05-13-1997	Wheat	
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	FOREIGN PATENT DOCUMENTS							
Examiner Cite Initials* No.	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages				
		Country Code ³ 'Number ⁴ 'Kind Code ⁵ (# known)	MM-DD-YYYY		Or Relevant Figures Appear	۳		
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Examiner Signature	/Eric Wai/	Date Considered	08/08/2007

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	Filing Date First Named Inventor Art Unit Examiner Name				

		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (block, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.	T²
/EW/		Daehyun Kim, Mainak Chaudhuri, and Mark Heinrick, Leveraging Cache Coherence in Active Memory Systems, Proceedings of the 16th ACM	
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/EW/		Mainak Chaudhuri, Daebyun Kim, and Mark Heinrich, Cache Coherence Protocol Design for Active Memory Systems, Proceedings of the 2002	
		Int'l Conference on Parallel and Distributed Processing Techniques and Applications, pages 83-89, Las Vagas, USA, June 2002.	
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Examiner	. /		Date	001001200	
Signature		/Eric Wai/	Considered	08/08/200	

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